

App. Serial No. 10/539,383  
Docket No.: NL021429

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**AUG 03 2007**

**In the Specification:**

Please amend Paragraph 0033 as indicated below.

[0033] The third stage 3 is substantially identical to the second stage 2 and comprises transistors  $T_4$  and  $T_5$ , the bases of which are coupled by an inductor  $L_5$ , and the collector of transistor  $T_4$  being coupled to the third bias circuit 6 and to the base of a base current compensation transistor  $T_{13}$ . The collector of transistor  $T_5$  is connected via an inductor  $L_6$  to a positive supply voltage  $V_s$ . At the collector of transistor  $T_5$  the output signal  $V_{out}$  of the power amplifier 100 is produced, in the embodiment shown via an impedance matching circuit 7.